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A Halder, S Bhattacharya, A Chatterjee - Proc. Intl. Test Conf, 2003 - itcprogramdev.org

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W Yang, H Carter, J Yan - Behavioral Modeling and Simulation Conference, 2004 - Citeseer

Page 1. A high-level VHDL-AMS model design methodology for **analog RF** LNA and Mixer ... 2001Pages:12 – 14 [5] Thomas H. Lee, The Design of CMOS **Radio-Frequency** Integrated Circuits ...[6] Behzad Razavi, **RF** microelectronics , Upper Saddle River, NJ: Prentice Hall, c1998 ...

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[PDF] ► Introduction to RF simulation and its application

KS Kundert - IEEE Journal of Solid-State Circuits, 1999 - Citeseer

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SS Akbay, A Chatterjee - Proc. VLSI Test Symposium, 2005 - users.ece.gatech.edu

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